

Model Number

ASM36M-*****

Features

- Very small housing
- High climatic resistance
- Up to 25 Bit multiturn
- SSI interface
- · Optically isolated interface
- Surge and reverse polarity protection

Description

This multiturn absolute encoder transmits a position value corresponding to the shaft setting via the SSI interface (Synchronous Serial Interface).

The ASM36M series are encoders with internal magnetic sampling.

The control module sends a start sequence to the absolute encoder to obtain the position data. The rotary encoder then sends the position data synchronous to the cycles of the control module. It is possible to select the counting direction with the function input.

Technical data

General specifications Detection type Device type **Electrical specifications** Operating voltage U_B Power consumption P_0 Output code Code course (counting direction) Interface Interface type Resolution Single turn Multiturn Overall resolution Transfer rate Cycle time Standard conformity Input 1 Input type Signal voltage Hiah I ow Input current Switch-on delay Input 2 Input type Signal voltage High Low Input current Signal duration Connection Connector Cable Standard conformity Degree of protection Connection side Shaft side Climatic testing Emitted interference Noise immunity Shock resistance Vibration resistance Ambient conditions Operating temperature Storage temperature Relative humidity Mechanical specifications

magnetic sampling Multiturn absolute encoder

4.5 ... 30 V DC approx. 2 W Gray code, binary code adjustable

SSI

up to 13 Bit (hysteresis: 0.1 °) up to 12 Bit up to 25 Bit 0.1 ... 2 MBit/s < 600 μs RS 422

Selection of counting direction (cw/ccw)

4.5 V ... U_B (cw descending) 0 ... 2 V or unconnected (cw ascending) < 6 mA < 1.1 s

zero-set (PRESET 1) with falling edge

4.5 V ... U_B 0 ... 2 V < 6 mA ≥ 1.1 s

M12 connector, 8-pin Ø6 mm, 4 x 2 x 0.14 mm², 1 m

acc. DIN EN 60529 cable models: IP54 connector models: IP65 IP54 DIN EN 60068-2-3, no moisture condensation EN 6100-6-4:2007 EN 6100-6-2:2005 DIN EN 60068-2-27, 100 g, 6 ms DIN EN 60068-2-6, 10 g, 10 ... 1000 Hz

cable, flexing: -5 ... 70 °C (-23 ... 158 °F), cable, fixed: -30 ... 70 °C (-22 ... 158 °F) connector models: -30 ... 85 °C (-22 ... 185 °F) cable models: -30 ... 70 °C (-22 ... 185 °F) connector models: -30 ... 85 °C (-22 ... 185 °F) 98 %, no moisture condensation

nickel-plated steel aluminum Stainless steel approx. 150 g , with cable max. 12000 min ⁻¹ 30 gcm² < 3 Ncm

110 N

Refer to "General Notes Relating to Pepperl+Fuchs Product Information".

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USA: +1 330 486 0001 fa-info@us.pepperl-fuchs.com Germany: +49 621 776 4411 fa-info@de.pepperl-fuchs.com

Material Housing

Flange

Rotational speed

Moment of inertia

Starting torque

Shaft load

Radial

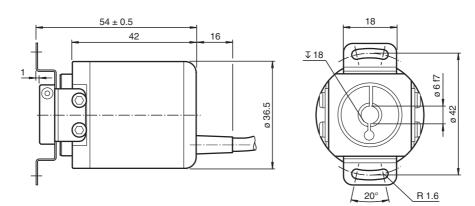
Shaft

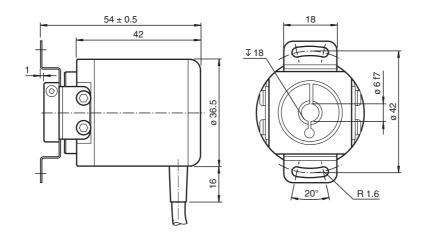
Mass

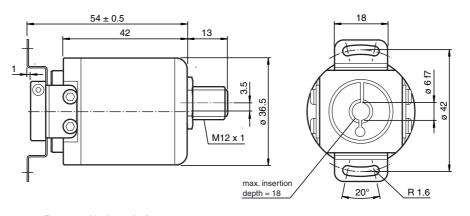
Singapore: +65 6779 9091 fa-info@sg.pepperl-fuchs.com



Dimensions







Recessed hollow shaft

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Electrical connection

Signal	Wire end	Connector
GND (encoder)	White	1
U _b (encoder)	Brown	2
Clock (+)	Green	3
Clock (-)	Yellow	4
Data (+)	Grey	5
Data (-)	Pink	6
Preset	Black or Blue	7
Counting direction	Red	8
Shielding	Shielding	Housing
Pinout	-	

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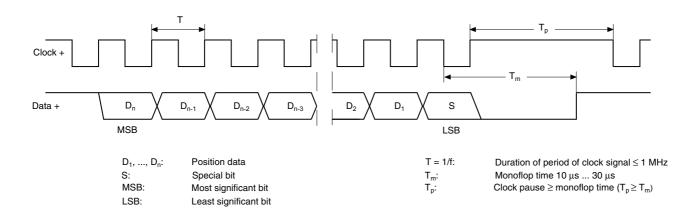


Description

The Synchronous Serial Interface was specially developed for transferring the output data of an absolute encoder to a control device. The control module sends a clock bundle and the absolute encoder responds with the position value.

Thus only 4 lines are required for the clock and data, no matter what the resolution of the rotary encoder is. The RS 422 interface is optically isolated from the power supply.

SSI signal course Standard



SSI output format Standard

- At idle status signal lines "Data +" and "Clock +" are at high level (5 V).
- The first time the clock signal switches from high to low, the data transfer in which the current information (position data (D_n) and special bit (S)) is stored in the encoder is introduced.
- The highest order bit (MSB) is applied to the serial data output of the encoder with the first rising pulse edge.
- The next successive lower order bit is transferred with each following rising pulse edge.
- After the lowest order bit (LSB) has been transferred the data line switches to low until the monoflop time T_m has expired.
- No subsequent data transfer can be started until the data line switches to high again or the time for the clock pause T_p has expired.
- After the clock sequence is complete, the monoflop time T_m is triggered with the last falling pulse edge.
- The monoflop time T_m determines the lowest transmission frequency.

SSI output format ring slide operation (multiple transmission)

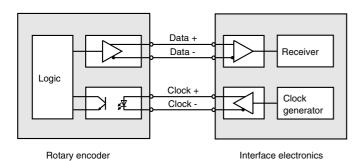
- In ring slide operation, multiple transmission of the same data word over the SSI interface makes it possible to offer the possibility of detecting transmission errors.
- In multiple transmission, 25 bits are transferred per data word in standard format.
- If the clock change is not interrupted after the last falling pulse edge, ring slide operation automatically becomes active. This means that the information that was stored at the time of the first clock change is generated again.
- After the first transmission, the 26th pulse controls data repetition. If the 26th pulse follows after an amount of time greater than the monoflop time T_m, a new current data word will be transmitted with the following pulses.



If the pulse line is exchanged, the data word is generated offset. Ring slide operation is possible up to max. 13 bits.

Block diagram

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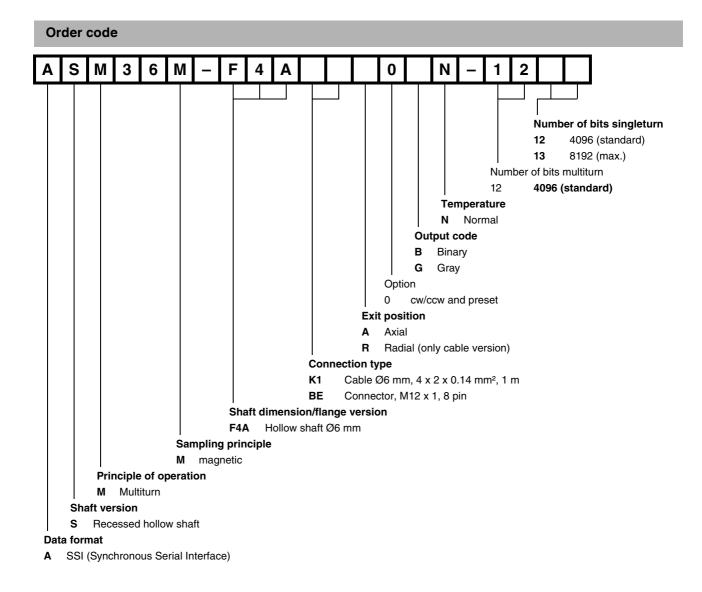


Line length

Line length in m	Baudrate in kHz
< 50	< 400
< 100	< 300
< 200	< 200
< 400	< 100

Germany: +49 621 776 4411 fa-info@de.pepperl-fuchs.com fa





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Pepperl+Fuchs Group www.pepperl-fuchs.com Germany: +49 621 776 4411 fa-info@de.pepperl-fuchs.com Singapore: +65 6779 9091 fa-info@sg.pepperl-fuchs.com

