

Model Number

ASM58-0

Features

- Industrial standard housing Ø58 mm
- 30 Bit multiturn
- · Data transfer up to 2 MBaud
- Optically isolated RS 422 interface
- Recessed hollow shaft
- Zero-set function

Description

This multiturn absolute encoder with modern fast technology transmits a position value corresponding to the shaft setting via the SSI interface (Synchronous Serial Interface). The maximum resolution of the ASM58 is maximum 65536 steps per revolution at 16384 revolutions.

The devices of the ASM58 series are equipped with a microcontroller.

The control module sends a clock bundle to the absolute encoder to obtain position data. The rotary encoder then sends the position data synchronous to the cycles of the control module.

It is possible to select the following items with function inputs

the counting direction and

• the zero-set function (preset value) The absolute encoder is mounted directly onto the application shaft, without any coupling. Rotation of the absolute encoder is prevented by a torque rest

The electrical connection is made by a 12-pin round plug connector. A version with a 1 m cable connector is also available.

	Detection type						
Device type							
	Functional safety related parameter						
	MTTF _d						
	Mission Time (T _M)						
	L _{10h}						
	Diagnostic Coverage (DC)						
Electrical specifications							
	Operating voltage U _B						
	No-load supply current I0						
	Linearity						
	Output code						
Þ	Code course (counting direction)						
	Interface						
	Interface type						
	Monoflop time						
i.	Resolution						
	Single turn						

Technical data General specifications

Detection type

Multiturn Overall resolution Transfer rate Voltage drop Standard conformity Input 1 Input type Signal voltage High Low Input current Switch-on delay Input 2 Input type Signal voltage Hiah I ow Input current Signal duration Switch-on delay Connection Connector Cable Standard conformity Degree of protection Climatic testing Emitted interference Noise immunity Shock resistance Vibration resistance Ambient conditions Operating temperature Storage temperature Mechanical specifications Material

Combination 1

Combination 2 (Inox)

Mass

Rotational speed Moment of inertia Starting torque Shaft load Angle offset Axial offset Radial offset up to 16 Bit 14 Bit up to 30 Bit 0.1 ... 2 MBit/s U_B - 2.5 V RS 422

1.9 E+11 at 6000 rpm and 20/40 N axial/radial shaft load

± 2 LSB at 16 Bit, ± 1 LSB at 13 Bit, ± 0,5 LSB at 12 Bit

cw descending (clockwise rotation, code course

Selection of counting direction (cw/ccw)

4.5 ... 30 V 0 ... 2 V < 6 mA < 10 ms

zero-set (PRESET 1)

photoelectric sampling

150 a 20 a

0%

SSI 20 ± 10 μs

4.5 ... 30 V DC max. 180 mA

descending)

Gray code, binary code

Multiturn absolute encoder

4.5 ... 30 V 0 ... 2 V < 6 mA ≥ 100 ms < 10 ms

> type 9416 (M23), 12-pin, type 9416L (M23), 12-pin Ø7 mm, 6 x 2 x 0.14 mm², 1 m

DIN EN 60529, IP65 DIN EN 60068-2-3, no moisture condensation DIN EN 61000-6-4 DIN EN 61000-6-2 DIN EN 60068-2-27, 100 g, 6 ms DIN EN 60068-2-6, 20 g, 10 ... 2000 Hz

-40 ... 85 °C (-40 ... 185 °F) -40 ... 85 °C (-40 ... 185 °F)

housing: powder coated aluminum flange: aluminum shaft: stainless steel housing: stainless steel flange: stainless steel shaft: stainless steel approx. 460 g (combination 1) approx. 800 g (combination 2) max. 12000 min ⁻¹ 50 gcm² < 5 Ncm

 \pm 0.9 $^{\circ}$ static: \pm 0.3 mm, dynamic: \pm 0.1 mm static: \pm 0.5 mm, dynamic: \pm 0.2 mm

Approvals and certificates

UL approval

cULus Listed, General Purpose, Class 2 Power Source

Release date: 2014-11-18 10:40

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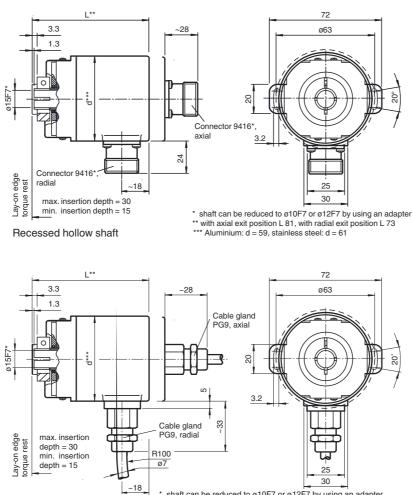
Date of issue: 2014-11-18

Refer to "General Notes Relating to Pepperl+Fuchs Product Information"

Pepperl+Fuchs Group www.pepperl-fuchs.com USA: +1 330 486 0001 fa-info@us.pepperl-fuchs.com Germany: +49 621 776 4411 fa-info@de.pepperl-fuchs.com Singapore: +65 6779 9091 fa-info@sg.pepperl-fuchs.com



Dimensions



Recessed hollow shaft

* shaft can be reduced to ø10F7 or ø12F7 by using an adapter ** with axial exit position L 81, with radial exit position L 73 *** Aluminium: d = 59, stainless steel: d = 61

Germany: +49 621 776 4411 fa-info@de.pepperl-fuchs.com

Singapore: +65 6779 9091 fa-info@sg.pepperl-fuchs.com



Electrical connection

Signal	Cable Ø7 mm, 12-core	Connector 9416, 12-pin	Connector 9416L, 12-pin	Explanation
GND (encoder)	White	1	1	Power supply
U _b (encoder)	Brown	2	8	Power supply
Clock (+)	Green	3	3	Positive cycle line
Clock (-)	Yellow	4	11	Negative cycle line
Data (+)	Grey	5	2	Positive transmission data
Data (-)	Pink	6	10	Negative transmission data
Reserved	Black	7	12	Not wired, reserved
V/R	Red	8	5	Input for selection of counting direction
PRESET 1	Blue	9	9	zero-setting input
Reserved	Violet	10	4	Not wired, reserved
Reserved	Grey/Pink	11	6	Not wired, reserved
Reserved	Red/Blue	12	7	Not wired, reserved
		9 8 10 7 12 6	8 7 11 6 5 4	

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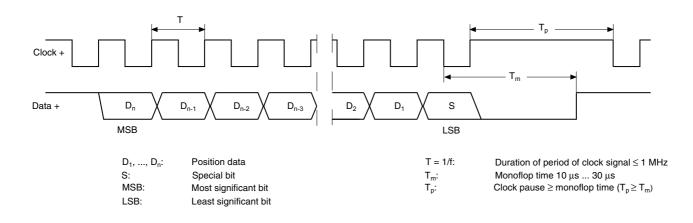


Description

The Synchronous Serial Interface was specially developed for transferring the output data of an absolute encoder to a control device. The control module sends a clock bundle and the absolute encoder responds with the position value.

Thus only 4 lines are required for the clock and data, no matter what the resolution of the rotary encoder is. The RS 422 interface is optically isolated from the power supply.

SSI signal course Standard



SSI output format Standard

- At idle status signal lines "Data +" and "Clock +" are at high level (5 V).
- The first time the clock signal switches from high to low, the data transfer in which the current information (position data (D_n) and special bit (S)) is stored in the encoder is introduced.
- The highest order bit (MSB) is applied to the serial data output of the encoder with the first rising pulse edge.
- The next successive lower order bit is transferred with each following rising pulse edge.
- After the lowest order bit (LSB) has been transferred the data line switches to low until the monoflop time T_m has expired.

The selection of the counting direction input (V/R) is activated with 0-level. The zero-set input (PRESET 1) is activated with 1-level.

- No subsequent data transfer can be started until the data line switches to high again or the time for the clock pause T_p has expired.
- After the clock sequence is complete, the monoflop time T_m is triggered with the last falling pulse edge.
- The monoflop time T_m determines the lowest transmission frequency.

SSI output format ring slide operation (multiple transmission)

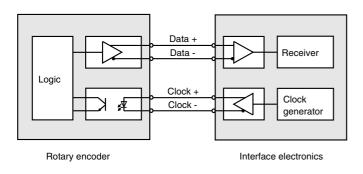
- In ring slide operation, multiple transmission of the same data word over the SSI interface makes it possible to offer the possibility of detecting transmission errors.
- In multiple transmission, 25 bits are transferred per data word in standard format.
- If the clock change is not interrupted after the last falling pulse edge, ring slide operation automatically becomes active. This means that the information that was stored at the time of the first clock change is generated again.
- After the first transmission, the 26th pulse controls data repetition. If the 26th pulse follows after an amount of time greater than the monoflop time T_m, a new current data word will be transmitted with the following pulses.



Inputs

If the pulse line is exchanged, the data word is generated offset. Ring slide operation is possible up to max. 13 bits.

Block diagram



Line length

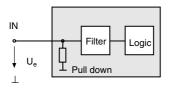
Line length in m	Baudrate in kHz
< 50	< 400
< 100	< 300
< 200	< 200
< 400	< 100

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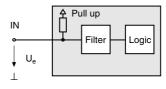
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zero-set input (PRESET 1)



Input for selection of counting direction (V/R)



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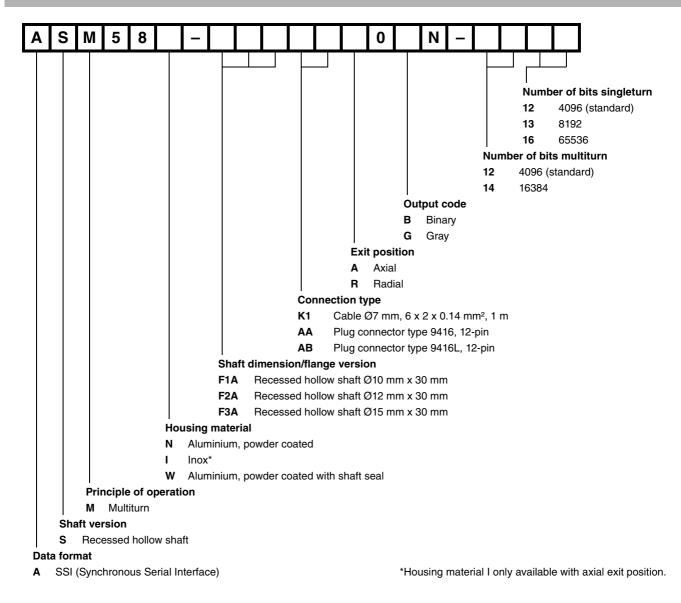
Accessories

Accessories Name/defining feature		Order code
Connectors	Cable socket	9416
	Cable socket	9416L

For additional information on the accessories, please see the "Accessories" section.



Order code



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Pepperl+Fuchs Group www.pepperl-fuchs.com USA: +1 330 486 0001 fa-info@us.pepperl-fuchs.com Germany: +49 621 776 4411 fa-info@de.pepperl-fuchs.com Singapore: +65 6779 9091 fa-info@sg.pepperl-fuchs.com



